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APPLICATION NO.	FII	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/667,897	09/23/2003		Chunyan Wang	6446-22US JA/AD/mb	8195
20988	7590	01/07/2005		EXAMINER	
OGILVY R			nguyen, khanh v		
1981 MCGII SUITE 1600		EGE AVENUE	ART UNIT	PAPER NUMBER	
MONTREA	L, QC H	3A2Y3	2817		
CANADA					_

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
		10/667,897	WANG, CHUNYAN			
Office Action	n Summary	Examiner	Art Unit			
		Khanh V. Nguyen	2817			
The MAILING DAT Period for Reply	E of this communication app	pears on the cover sheet with	the correspondence address			
A SHORTENED STATU THE MAILING DATE OF Extensions of time may be availater SIX (6) MONTHS from the If the period for reply specified a If NO period for reply is specified Failure to reply within the set or	THIS COMMUNICATION. able under the provisions of 37 CFR 1.1 mailing date of this communication. bove is less than thirty (30) days, a repl I above, the maximum statutory period to extended period for reply will, by statute later than three months after the mailing	Y IS SET TO EXPIRE 3 MO 36(a). In no event, however, may a repl y within the statutory minimum of thirty (will apply and will expire StX (6) MONTH cause the application to become ABAN g date of this communication, even if tim	ly be timely filed 30) days will be considered timely. IS from the mailing date of this communication. NDONED (35 U.S.C.§ 133).			
Status						
1) Responsive to con	nmunication(s) filed on 23 S	entember 2003				
2a) ☐ This action is FINA	· · · · · · · · · · · · · · · · · · ·					
<u>′</u>	s, prosecution as to the merits is					
•	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4a) Of the above cl 5) ☐ Claim(s) is/3 6) ☑ Claim(s) <u>1-4 and 1</u> 7) ☑ Claim(s) <u>5-9 and 1</u>	<i>0-15</i> is/are rejected.	wn from consideration.				
Application Papers						
10)⊠ The drawing(s) filed Applicant may not re Replacement drawin	quest that any objection to the g sheet(s) including the correct	are: a)⊠ accepted or b)□ of drawing(s) be held in abeyance tion is required if the drawing(s)	objected to by the Examiner. e. See 37 CFR 1.85(a). is objected to. See 37 CFR 1.121(d). Office Action or form PTO-152.			
Priority under 35 U.S.C. § 1	19					
a) All b) Some 1. Certified cop 2. Certified cop 3. Copies of the application for	* c) None of: vies of the priority document vies of the priority document e certified copies of the prior rom the International Burea	s have been received in Apprity documents have been re	olication No eceived in this National Stage			
Attachment(s)						
1) Notice of References Cited (F			mmary (PTO-413)			
 Notice of Draftsperson's Pate Information Disclosure Stater Paper No(s)/Mail Date 	nt Drawing Review (PTO-948) nent(s) (PTO-1449 or PTO/SB/08)		Mail Date rmal Patent Application (PTO-152)			

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-4, 10-15 are rejected under 35 U.S.C. 102(b) as being anticipated by Kronmueller et al. (6,414,552).

Regarding claims 1, 10, Kronmueller et al. (Fig. 3) disclose an amplifier circuit having an input current (I2) and an output current (I3) comprising: a transistor (N3) can be read as a first semiconductor device having a gate control terminal and drain and source current terminals; a transistor (N4) can be read as a second semiconductor device having a gate control terminal and drain and source current terminals; and a transistor (N2) can be read as a claimed circuitry, wherein the reference circuit is capable of operating the functions claimed since the reference circuit discloses similar structure as claimed invention.

Regarding claims 2, 3, 11, 12, wherein the first semiconductor device (N3) and the second semiconductor device (N4) are capable of biasing recited regions by the circuitry (N2) since the reference circuit discloses similar structure as claimed invention.

Regarding claims 4, 13, wherein transistor (N2) can be read as a third semiconductor device having a gate control terminal and drain and source current terminals, in series with the first semiconductor device (N3) having the functions thereof.

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Regarding claim 14, wherein the first and second semiconductor devices (N3, N4) are metal-oxide field effect transistors.

Regarding claim 15, the first and second semiconductor devices (N3, N4) are identical since they are mirrored to one another.

Allowable Subject Matter

Claims 5-9, 16-19 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 5-9, 16-19 call for, among others, a feedback system having the functions thereof.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The additional references (Swanson (4,477,782); Pauls (6,169,456); Tsubaki (6,528,981)) disclose amplifier circuit having current mirror.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khanh V. Nguyen whose telephone number is (571) 272-1767. The examiner can normally be reached from 8:00 AM - 3:30 PM EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571) 272-1769. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for After Final communications.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

KHANH V. NGUYEN PRIMARY EXAMINER

Maul Oulquyon

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